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<p>Substitute for form 1449A/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(use as many sheets as necessary)</p>				<p align="center">Complete if Known</p>	
				Application Number	10/674,835
				Filing Date	September 29, 2003
				First Named Inventor	Jose S. Niell
				Art Unit	2129
				Examiner Name	
				Attorney Docket Number	42P17022

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature	/Omar Fernandez Rivas/	Date Considered	12/8/2006
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NON PATENT LITERATURE DOCUMENTS					
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OFR		HNATEK, EUGENE R., "A User's Handbook of Semiconductor Memories", 1977, A Wiley-Interscience Publication, John Wiley & Sons, Inc., Pgs. 163-181			
OFR		TAUB, HERBERT et al., "Digital Integrated Electronics", 1977, McGraw-Hill Book Company, Pgs. 394-396			
OFR		MANO, M. MORRIS, "Computer System Architecture, Second Edition", 1982, Prentice-Hall, Inc., Pgs. 426-428			
OFR		HOROTITZ, PAUL et al., "The Art of Electronics, Second Edition", 1989, Cambridge University Press, Pg. 541			
OFR		"CMOS BiCMOS Data Book", Cypress Semiconductor Corporation, February 1, 1989, Pgs. 5-34 through 5-57			
OFR		"Databook Data Converters - DSP Products", TRW LSI Products Inc., 1990, Pgs. 23-52			
OFR		DORF, RICHARD C., "The Electrical Engineering Handbook", 1993, CRC Press, Inc., Pgs. 1725-1727			

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